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In the Application of:	Robert A. Aekins, et al.)
On:	DUAL REACTANCE LOW NOISE MODULAR CONNECTOR INSERT) Conf. No.: 8240)
Serial No.:	09/982,073) Group Art Unit: 2833)
Filed On:	October 17, 2001) Examiner: Brigitte R. Hammond)
) Docket No. 695695.0086

Hartford, Connecticut, December 18, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §1.56, the materials listed on the attached form PTO-1449 are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application.



The Supplemental Information Disclosure Statement submitted herewith is being filed, to the best of applicant's knowledge, before the mailing date of any of a final action under §1.113, or a notice of allowance under §1.311.

Each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

The filing of this Supplemental Information Disclosure Statement shall not be construed to be a representation that a search has been conducted, nor shall it be construed as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

It is respectfully requested that the Examiner return a copy of the attached form PTO-1449 with initials or other appropriate marks indicating consideration of the cited materials.

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No fee is believed associated with this submission. If any fee is required, or to cover deficiency in fees already paid, authorization is hereby given to charge such to deposit account 50-1402.

Dated: December 18, 2003

Respectfully submitted,

By:


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Sheet 1 of Sheet 1

Form PTO-1449 (modified 2/91)	U.S. DEPT OF COMMERCE Patent and Trademark Office	Attorney Docket Number: 695695.0086	Serial No.: 09/982,073
<p>INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)</p> <p>DUAL REACTANCE LOW NOISE MODULAR CONNECTOR INSERT</p>			
Applicant: Robert A. Aekins, et al.			
Filing date: October 17, 2001	Group Art Area 2833		

U.S. PATENT DOCUMENTS

Examiner Initial	Patent Number	Date	Name	Class	Sub-class	Filing date if appropriate
	5,674,093	October 7, 1997	Vaden	439	676	
	6,113,418	September 5, 2000	Kjeldahl	439	405	
	6,155,881	December 5, 2000	Arnett et al.	439	676	

FOREIGN PATENT DOCUMENTS

	Document number	Date	Country	Class	Subclass	Translation
	EP 0 674 364 A1	September 27, 1995	EP	H01R	23/02	
	EP 0955 703A2	November 10, 1999	EP	H01R	23/00	
	EP 0955 703A3	November 14, 2001	EP	H01R	23/00	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner:	Date Considered:
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	